

## SiTime MEMS Timing Benefits

### Complete MEMS clock tree

- Precision MEMS OCXO
- Stratum 3 Super-TCXO
- 10 Output Clock Generator

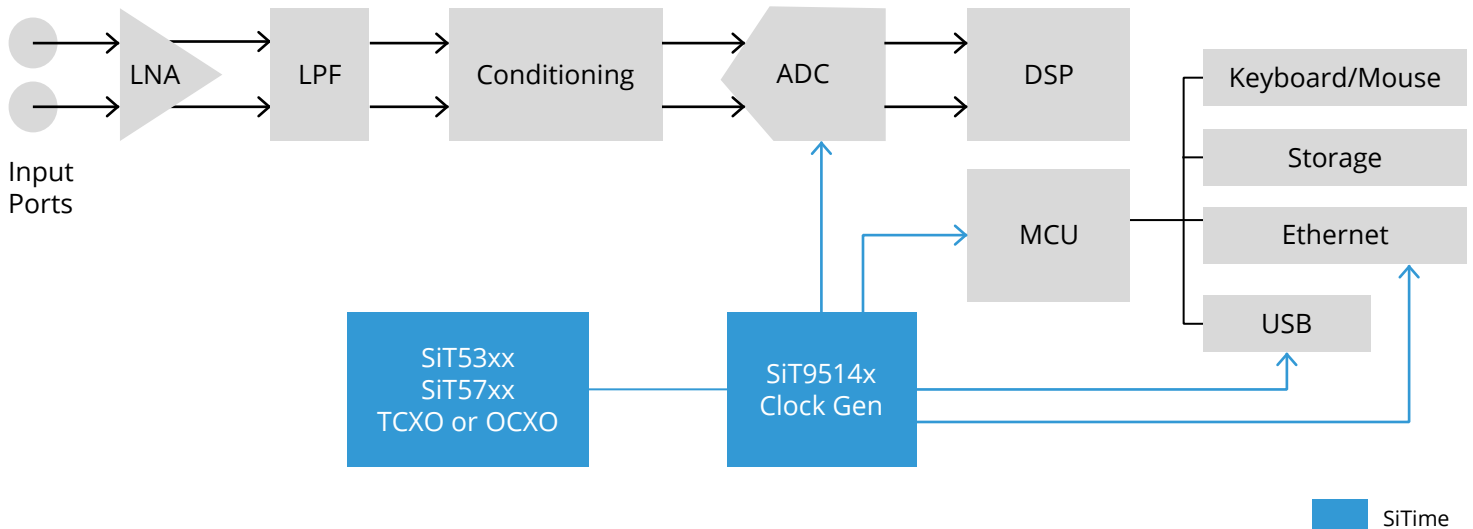
### Most robust in real world conditions

- Stable over a lifetime:  $\pm 70$  ppb over 10 years
- I<sup>2</sup>C monitoring for simplified compensation
- Integrated MEMS saves space, simplifies BOM

### Easy to use, built to last

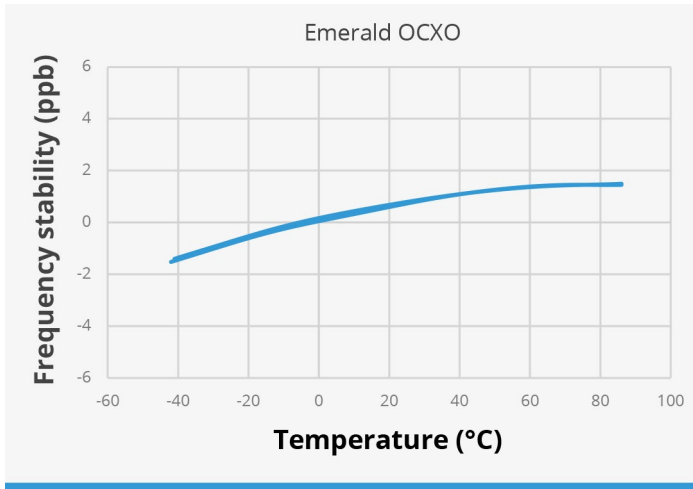
- Custom configured solution
- No quartz reliability issues
- 1 billion hour MTBF

## MEMS Timing Solution for Digital Oscilloscopes

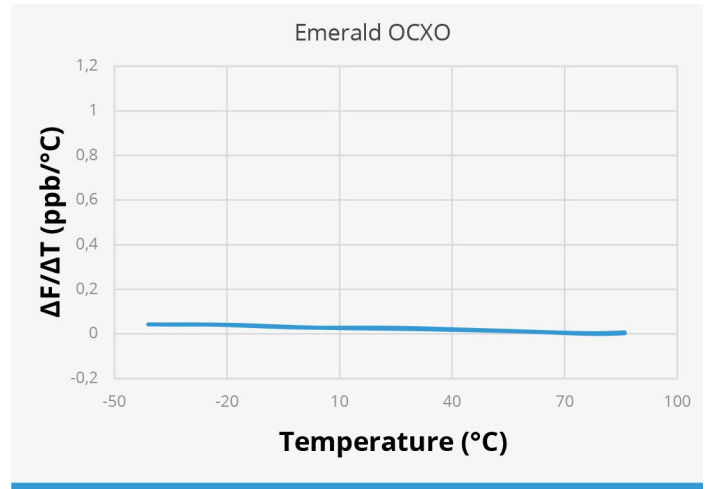


Devices	Type	Function	Key Features
<a href="#">SiT515x</a>	Super-TCXO	Stability engine for clock synthesis	1 to 220 MHz, $\pm 50$ to $\pm 250$ ppb, $\pm 1$ ppb/ $^{\circ}$ C, $\pm 80$ ppb 20-year aging, 70g vibration survivability
<a href="#">SiT5711</a>	OCXO		1 to 60 MHz, $\pm 5$ ppb, $\pm 70$ ppb aging over 10 years
<a href="#">SiT5721</a>	Digitally Controlled OCXO		1 to 60 MHz, $\pm 70$ ppb aging over 10 years, I <sup>2</sup> C Programmable
<a href="#">SiT9514x</a>	Clock Generator/ Jitter Attenuator	Flexible clock synthesis and jitter attenuation	8 KHz to 2.1 GHz, 120 fs jitter for SyncE, 4 inputs and 8 outputs

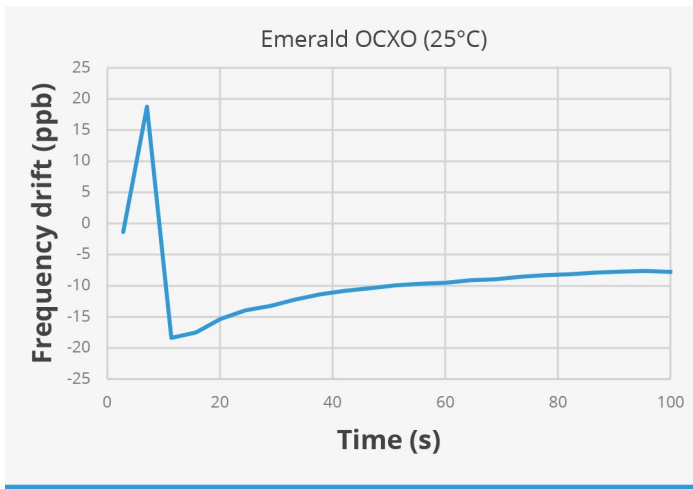
## Better Stability



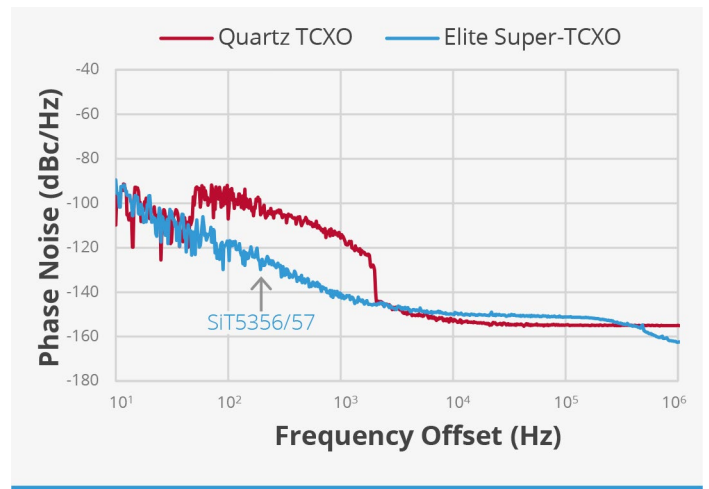
## Better Frequency Slope



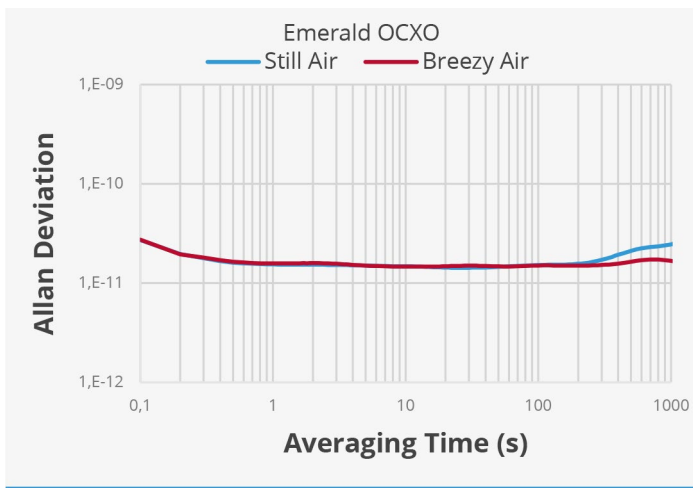
## Faster Warm Up



## Better Vibration Resistance



## Better Allan Deviation



## Better Aging

