Si Time [®]	Title:	Performance Report SiT1602B, 66.66666MHz			
	Туре:	Performance report	Rev:	1.0	
	Orig:		Date:	Mar 31, 2014	

This report contains sample performance data for SiT1602B-66.66666MHz.

Conditions:

- Frequency 66.66666 MHz
- Vdd 1.8V, 2.5V, 2.8V, 3.0V, 3.3V
- Temperature 25℃
- Termination:
 - No load for IDD
 - $\circ~~50\Omega$ to GND for phase noise
 - $\circ \quad 15 pF \text{ for other tests} \quad$

Equipment:

- Agilent DSA90604 oscilloscope (6GHz, 20Gsps)
 - Period jitter, waveform, rise/fall time, duty cycle, amplitude
- Agilent E5052B Signal Source Analyzer
 - Phase noise, integrated phase jitter
- Power supply current
 - Agilent 34401A DMM

Data:

- Random Phase jitter, Period Jitter, Duty cycle, Rise/Fall time, Amplitude, Idd
- Output waveforms
- Frequency stability versus temperature

Parameter	Units	Voltage					
	Onits	1.8 V	2.5 V	2.8 V	3.0 V	3.3 V	
Random Phase jitter (900kHz - 20MHz)	ps, rms	0.61	0.66	0.69	0.68	0.69	
Random Phase jitter (12kHz - 20MHz)	ps, rms	1.38	1.41	1.44	1.45	1.46	
Period jitter	ps, rms	1.78	1.60	1.54	1.57	1.53	
Period jitter (10,000 cycles)	ps, pk-pk	12.9	11.6	11.3	11.2	11.1	
Duty cycle	%	49.9	49.8	50.3	50.6	51.0	
Rise time (20% - 80%)	ns	1.24	1.02	0.94	1.01	0.94	
Fall time (80% - 20%)	ns	1.25	0.99	0.92	0.98	0.93	
Amplitude	V	1.76	2.44	2.72	2.98	3.25	
Current consumption (no load, output enabled)	mA	4.21	4.49	4.63	4.69	4.84	
Current consumption (no load, output disabled)	mA	3.53	3.60	3.65	3.69	3.77	

Table 1. Performance data

Si Time [®]	Title:	Performance Report SiT1602B, 66.66666MHz		
	Туре:	Performance report	Rev:	1.0
	Orig:		Date:	Mar 31, 2014

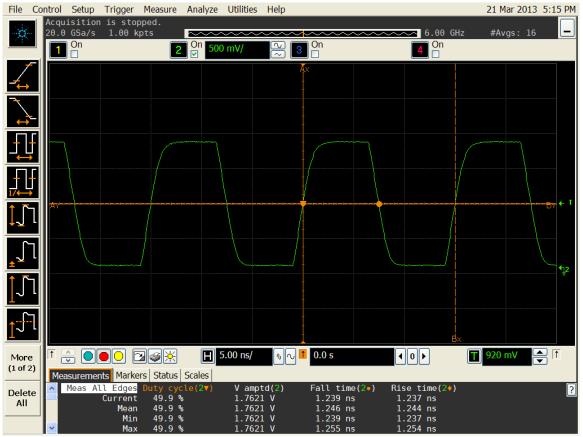


Figure 1. Duty cycle, Rise/Fall time and Amplitude 1.8V

Page 2 of 11

Si Time [®]	Title:	Performance Report SiT1602B, 66.66666MHz		
	Туре:	Performance report	Rev:	1.0
	Orig:		Date:	Mar 31, 2014

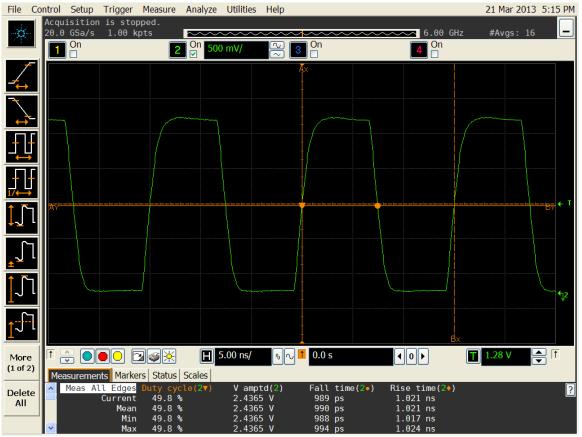


Figure 2. Duty cycle, Rise/Fall time and Amplitude 2.5V

Page 3 of 11

Si Time [®]	Title:	Performance Report SiT1602B, 66.66666MHz		
	Туре:	Performance report	Rev:	1.0
	Orig:		Date:	Mar 31, 2014

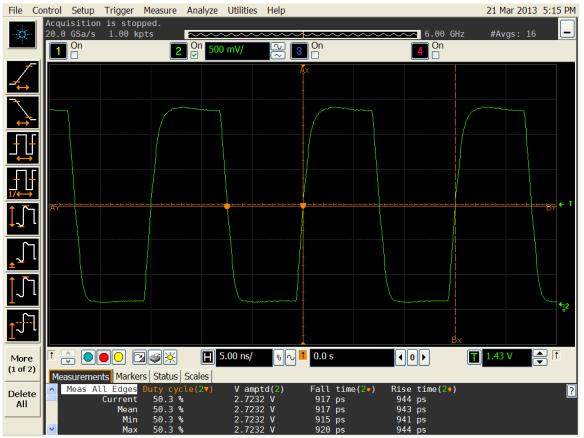


Figure 3. Duty cycle, Rise/Fall time and Amplitude 2.8V

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	Туре:	Performance report	Rev:	1.0
	Orig:		Date:	Mar 31, 2014



Figure 4. Duty cycle, Rise/Fall time and Amplitude 3.0V

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	Туре:	Performance report	Rev:	1.0
	Orig:		Date:	Mar 31, 2014



Figure 5. Duty cycle, Rise/Fall time and Amplitude 3.3V

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	Туре:	Performance report	Rev:	1.0		
	Orig:		Date:	Mar 31, 2014		

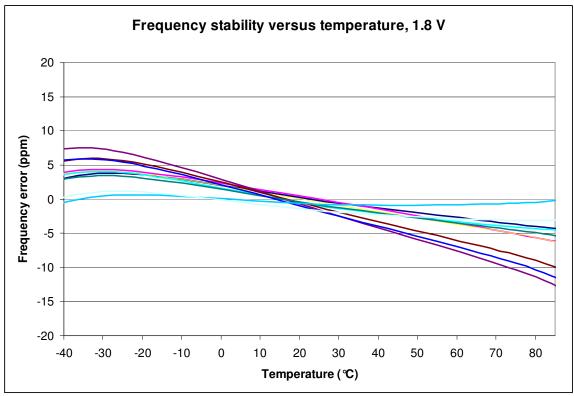


Figure 6. Frequency stability* versus temperature, 1.8 V

*Please note that frequency stability in SiTime devices is not depended on output frequency.

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	Orig:		Date:	Mar 31, 2014	

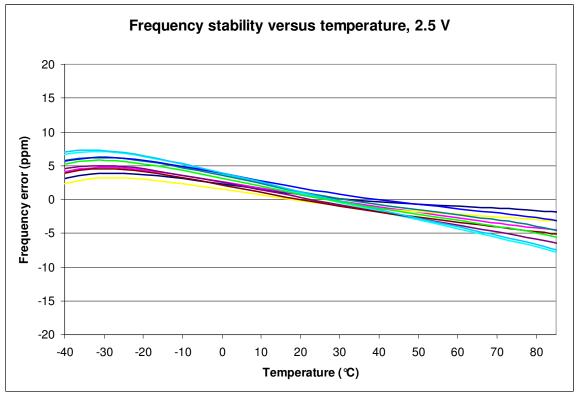


Figure 7. Frequency stability versus temperature, 2.5 V

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	Orig:		Date:	Mar 31, 2014		

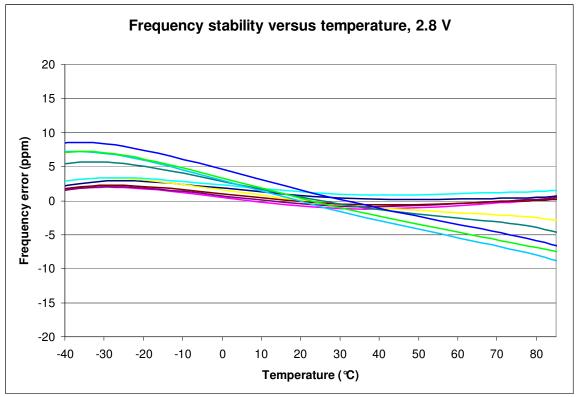


Figure 8. Frequency stability versus temperature, 2.8 V

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	Type:	Performance report	Rev:	1.0		
	Orig:		Date:	Mar 31, 2014		

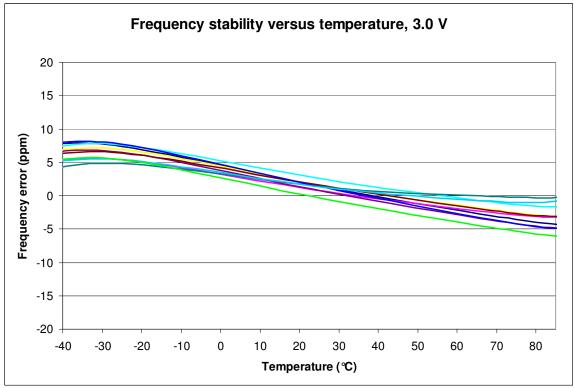


Figure 9. Frequency stability versus temperature, 3.0 V

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	Туре:	Performance report	Rev:	1.0		
	Orig:		Date:	Mar 31, 2014		

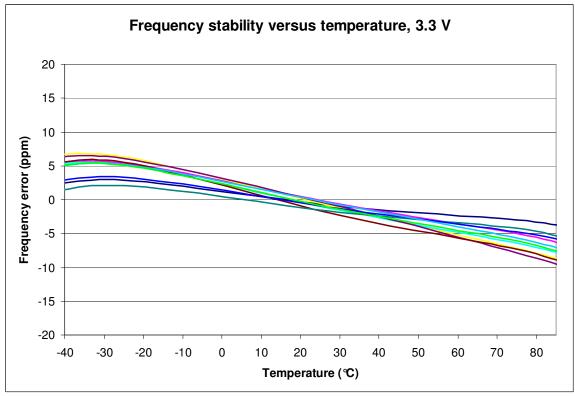


Figure 10. Frequency stability versus temperature, 3.3 V