<b>Si</b> Time <sup>®</sup>	Title:	Performance Report SiT1602B, 77.76MHz			
	Туре:	Performance report	Rev:	1.0	
	Orig:		Date:	Mar 31, 2014	

# This report contains sample performance data for SiT1602B-77.76MHz.

## Conditions:

- Frequency 77.76 MHz
- Vdd 1.8V, 2.5V, 2.8V, 3.0V, 3.3V
- Temperature 25℃
- Termination:
  - No load for IDD
  - $\circ~~50\Omega$  to GND for phase noise
  - $\circ \quad 15 pF \text{ for other tests} \quad$

# Equipment:

- Agilent DSA90604 oscilloscope (6GHz, 20Gsps)
  - o Period jitter, waveform, rise/fall time, duty cycle, amplitude
- Agilent E5052B Signal Source Analyzer
  - Phase noise, integrated phase jitter
- Power supply current
  - Agilent 34401A DMM

## Data:

- Random Phase jitter, Period Jitter, Duty cycle, Rise/Fall time, Amplitude, Idd
- Output waveforms
- Frequency stability versus temperature

Parameter	Units	Voltage					
	Units	1.8 V	2.5 V	2.8 V	3.0 V	3.3 V	
Random Phase jitter (900kHz - 20MHz)	ps, rms	0.59	0.64	0.64	0.64	0.65	
Random Phase jitter (12kHz - 20MHz)	ps, rms	1.35	1.38	1.39	1.39	1.40	
Period jitter	ps, rms	1.79	1.64	1.63	1.64	1.61	
Period jitter (10,000 cycles)	ps, pk-pk	13.0	12.0	11.7	11.9	11.6	
Duty cycle	%	49.9	49.7	50.3	50.9	51.2	
Rise time (20% - 80%)	ns	1.24	1.05	0.97	1.01	0.96	
Fall time (80% - 20%)	ns	1.25	1.01	0.94	0.99	0.96	
Amplitude	V	1.77	2.47	2.78	2.98	3.30	
Current consumption (no load, output enabled)	mA	4.34	4.67	4.82	4.89	5.07	
Current consumption (no load, output disabled)	mA	3.53	3.60	3.66	3.70	3.78	

#### Table 1. Performance data

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	Orig:		Date:	Mar 31, 2014

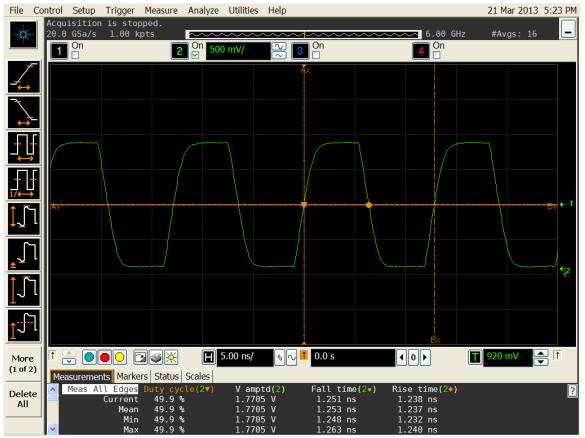


Figure 1. Duty cycle, Rise/Fall time and Amplitude 1.8V

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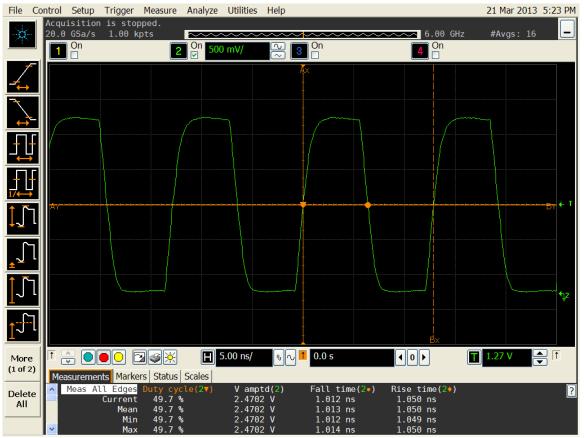


Figure 2. Duty cycle, Rise/Fall time and Amplitude 2.5V

Page 3 of 11

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	Туре:	Performance report	Rev:	1.0
	Orig:		Date:	Mar 31, 2014



Figure 3. Duty cycle, Rise/Fall time and Amplitude 2.8V

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Figure 4. Duty cycle, Rise/Fall time and Amplitude 3.0V

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	Orig:		Date:	Mar 31, 2014

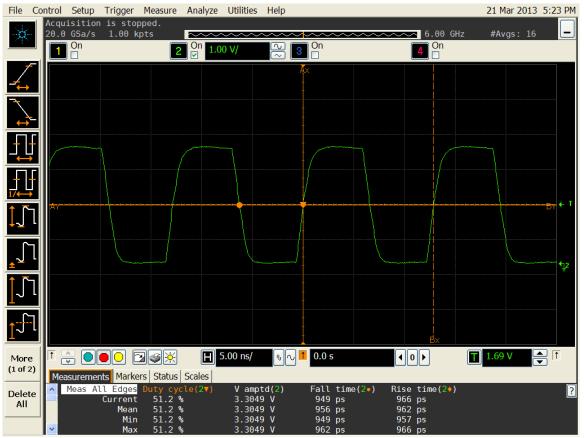


Figure 5. Duty cycle, Rise/Fall time and Amplitude 3.3V

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	Orig:		Date:	Mar 31, 2014	

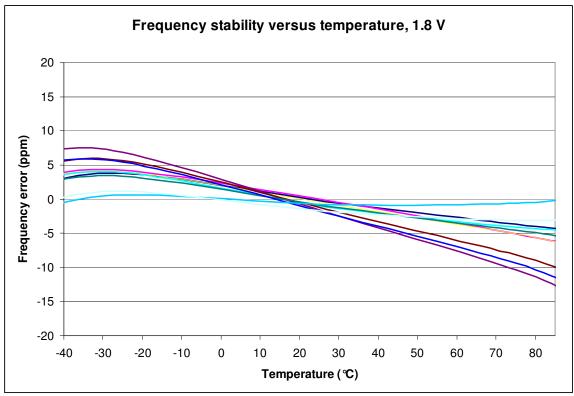


Figure 6. Frequency stability\* versus temperature, 1.8 V

\*Please note that frequency stability in SiTime devices is not depended on output frequency.

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<b>Si</b> Time <sup>®</sup>	Туре:	Performance report	Rev:	1.0	
	Orig:		Date:	Mar 31, 2014	

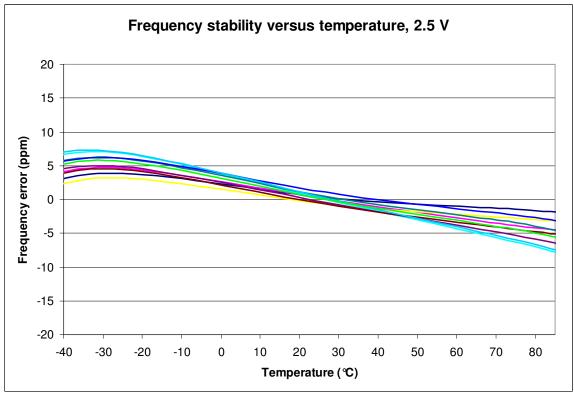


Figure 7. Frequency stability versus temperature, 2.5 V

Page 8 of 11

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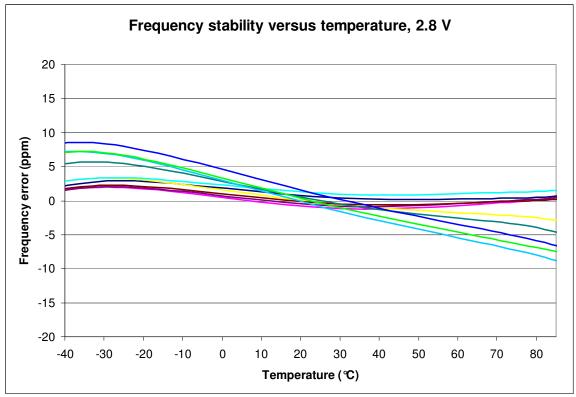


Figure 8. Frequency stability versus temperature, 2.8 V

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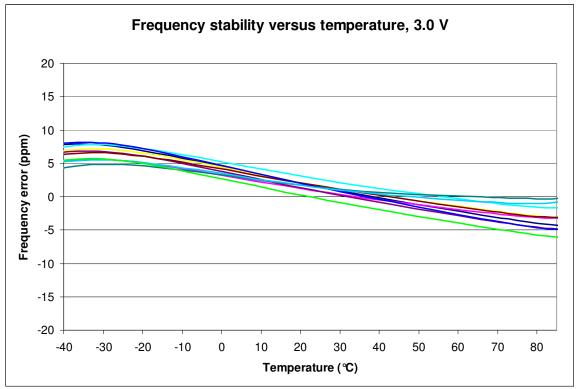


Figure 9. Frequency stability versus temperature, 3.0 V

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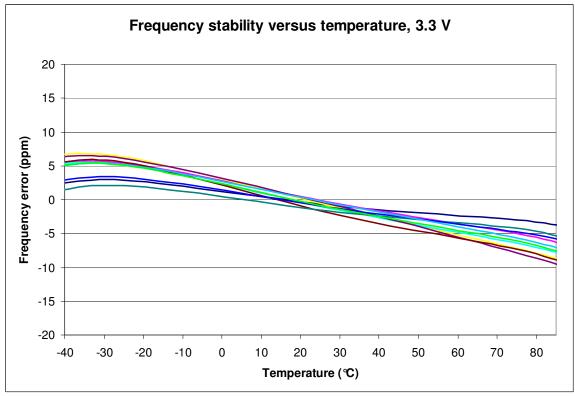


Figure 10. Frequency stability versus temperature, 3.3 V