	Title:	Performance Report SiT1618B, 7.3728MHz		
	Type:	Performance report	Rev:	1.0
	Orig:		Date:	Nov 25, 2014

This report contains sample performance data for SiT1618B-7.3728MHz.

Conditions:

- Frequency 7.3728 MHz
- Vdd 1.8V, 2.5V, 2.8V, 3.0V, 3.3V
- Temperature 25 °C
- Termination:
 - o No load for IDD
 - o 50Ω to GND for phase noise
 - o 15pF for other tests

Equipment:


- Agilent DSA90604 oscilloscope (6GHz, 20Gsps)
 - o Period jitter, waveform, rise/fall time, duty cycle, amplitude
- Agilent E5052B Signal Source Analyzer
 - o Phase noise, integrated phase jitter
- Power supply current
 - o Agilent 34401A DMM

Data:

- Random Phase jitter, Period Jitter, Duty cycle, Rise/Fall time, Amplitude, Idd
- Output waveforms
- Frequency stability versus temperature

Table 1. Performance data

Parameter	Units	Voltage				
		1.8 V	2.5 V	2.8 V	3.0 V	3.3 V
Period jitter	ps, rms	2.08	1.88	1.85	1.84	1.83
Period jitter (10,000 cycles)	ps, pk-pk	15.3	13.8	13.7	13.9	13.5
Duty cycle	%	50.0	50.0	50.0	50.1	50.1
Rise time (20% - 80%)	ns	1.24	1.01	0.91	0.98	0.90
Fall time (80% - 20%)	ns	1.26	0.98	0.90	0.97	0.92
Amplitude	V	1.79	2.49	2.79	3.02	3.32
Current consumption (no load, output enabled)	mA	3.34	3.41	3.44	3.46	3.50
Current consumption (no load, output disabled)	mA	3.33	3.39	3.45	3.49	3.56

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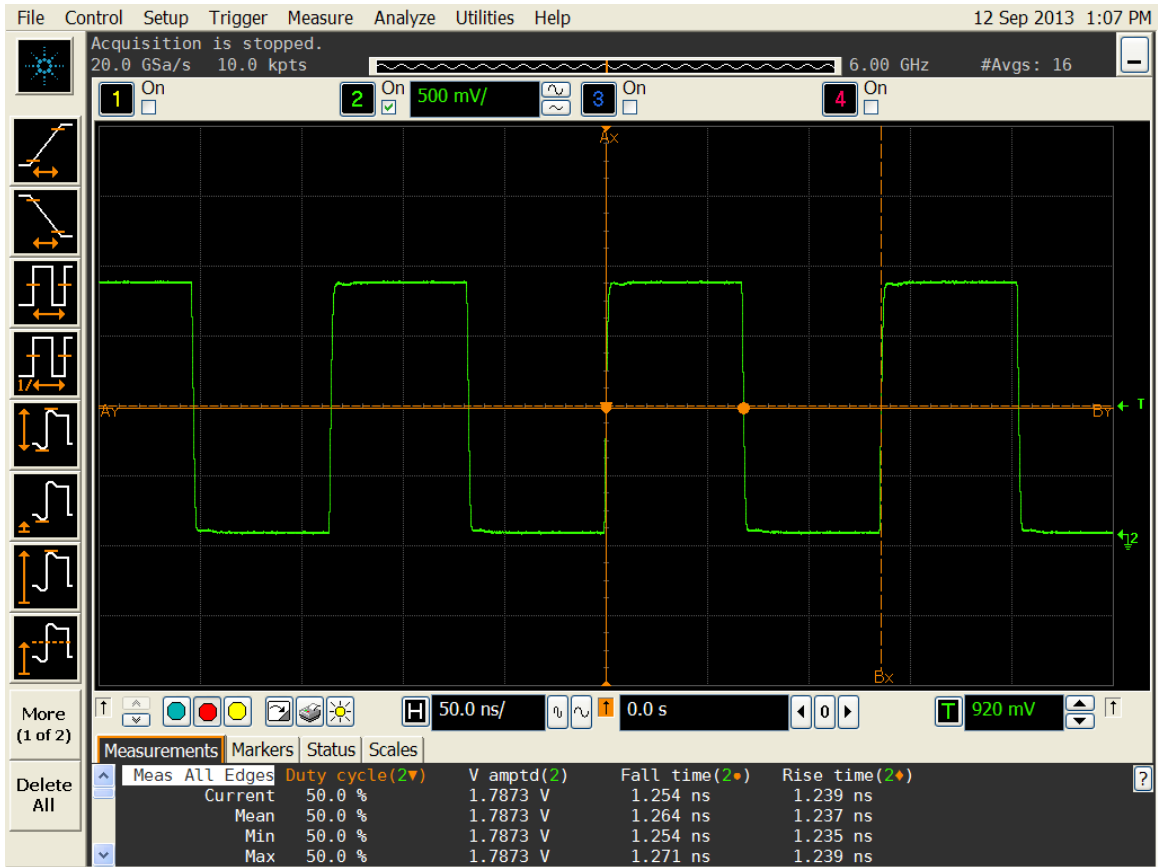



Figure 1. Duty cycle, Rise/Fall time and Amplitude 1.8V

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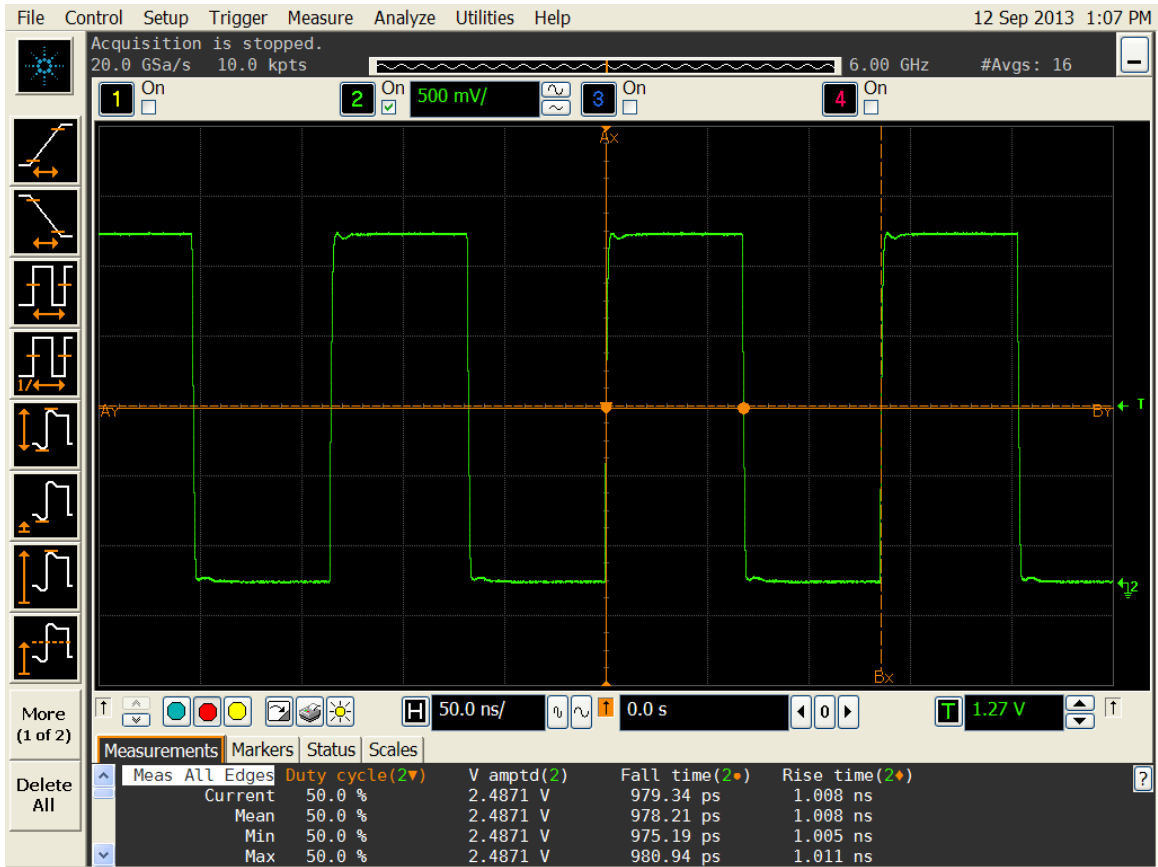



Figure 2. Duty cycle, Rise/Fall time and Amplitude 2.5V

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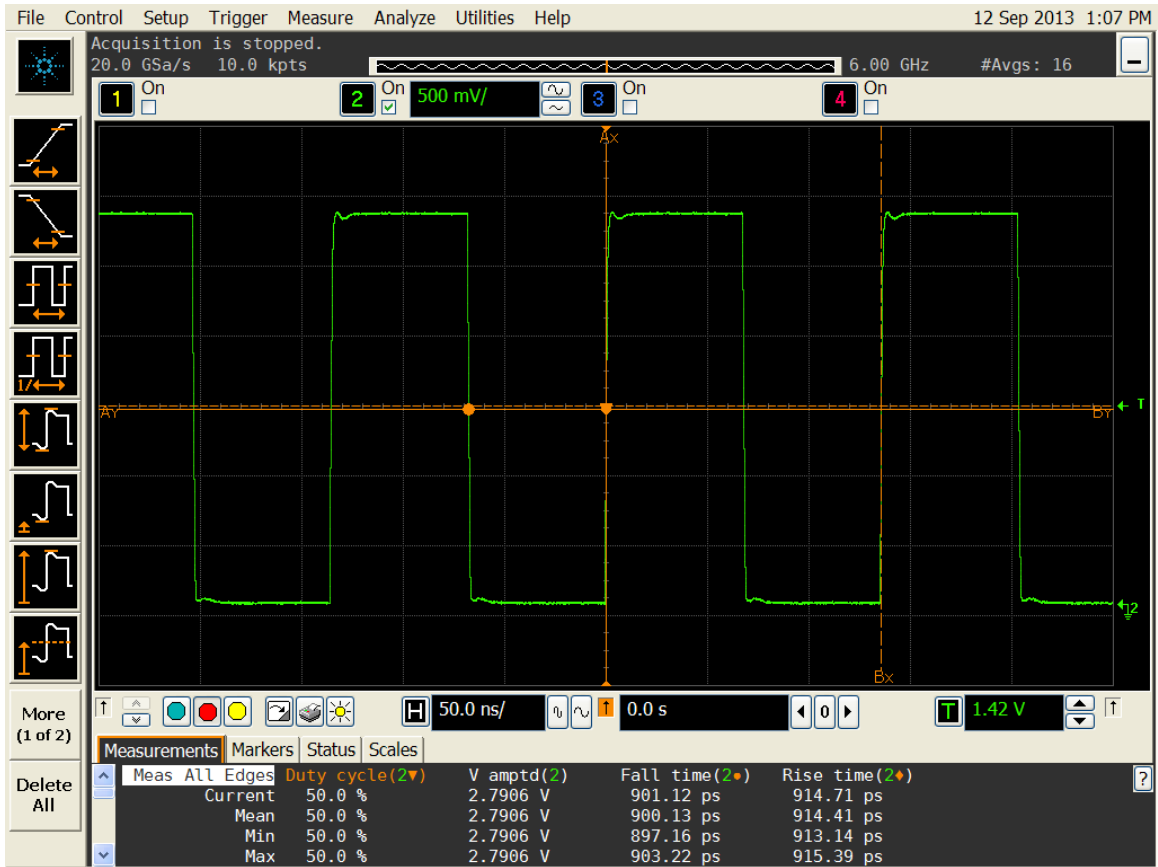



Figure 3. Duty cycle, Rise/Fall time and Amplitude 2.8V

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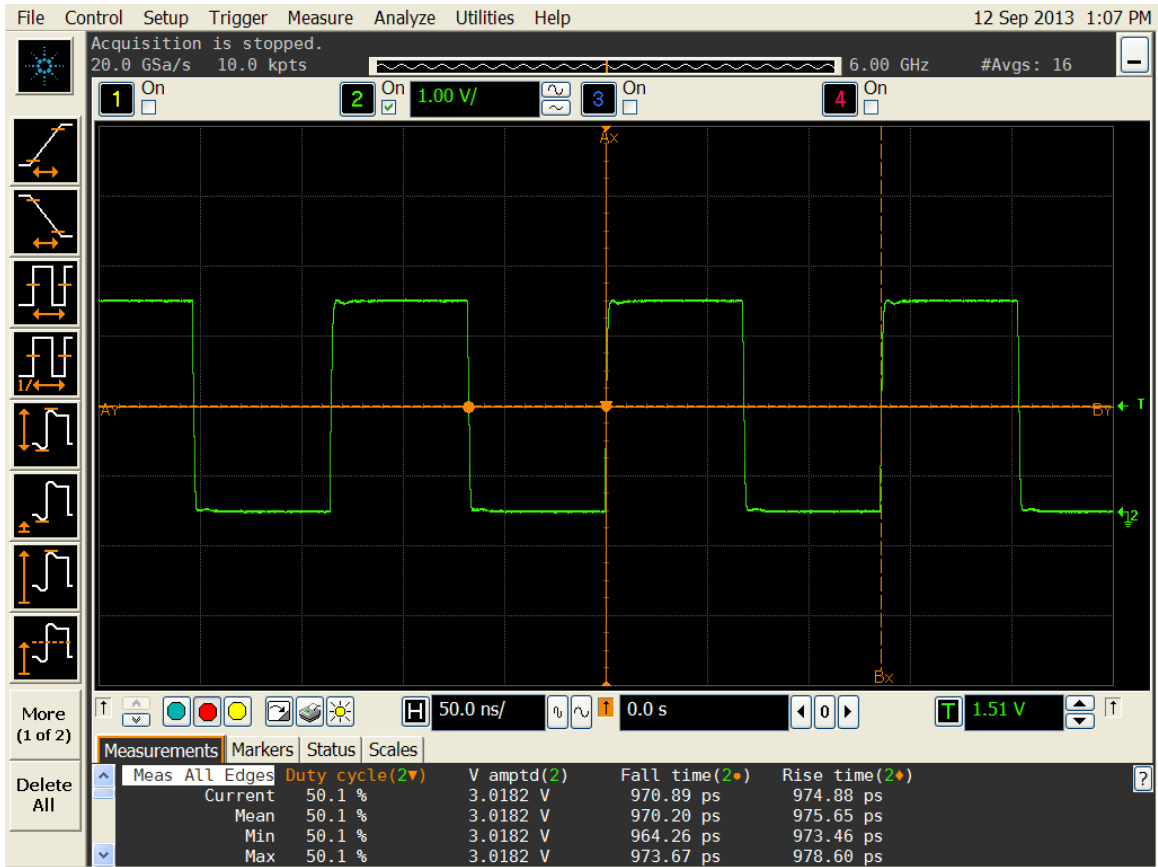



Figure 4. Duty cycle, Rise/Fall time and Amplitude 3.0V

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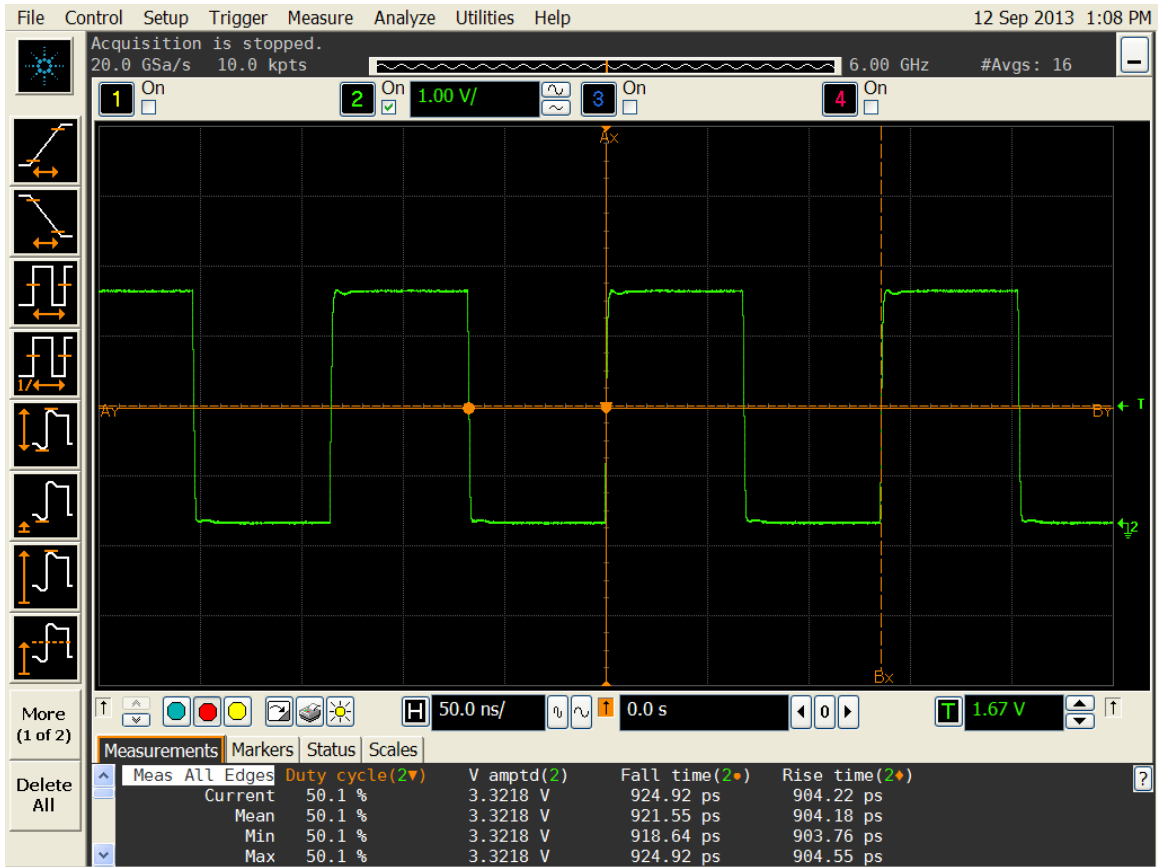



Figure 5. Duty cycle, Rise/Fall time and Amplitude 3.3V

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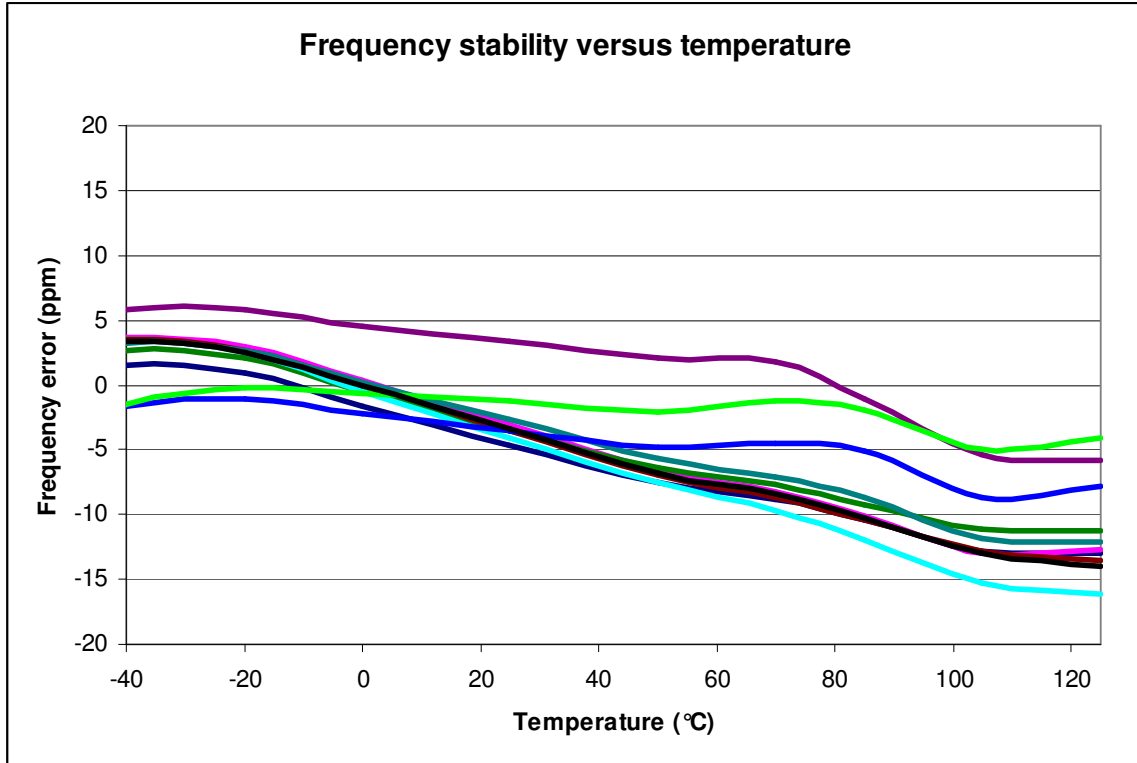


Figure 6. Frequency stability* versus temperature

*Please note that frequency stability in SiTime devices is not depended on output frequency.