

Title:	Performance Report SiT2001B, 40MHz			
Type:	Performance report	Rev:	1.0	
Orig:		Date:	Mar 31, 2014	

This report contains sample performance data for SiT2001B-40MHz.

Conditions:

- Frequency 40 MHz
- Vdd 1.8V, 2.5V, 2.8V, 3.0V, 3.3V
- Temperature 25 °C
- Termination:
 - No load for IDD
 - \circ 50 Ω to GND for phase noise
 - o 15pF for other tests

Equipment:

- Agilent DSA90604 oscilloscope (6GHz, 20Gsps)
 - o Period jitter, waveform, rise/fall time, duty cycle, amplitude
- Agilent E5052B Signal Source Analyzer
 - o Phase noise, integrated phase jitter
- Power supply current
 - o Agilent 34401A DMM

Data:

- Random Phase jitter, Period Jitter, Duty cycle, Rise/Fall time, Amplitude, Idd
- Output waveforms
- Frequency stability versus temperature

Table 1. Performance data

Parameter	Units	Voltage				
i didilietei	Offics	1.8 V	2.5 V	2.8 V	3.0 V	3.3 V
Random Phase jitter (900kHz - 20MHz)	ps, rms	0.67	0.69	0.69	0.69	0.69
Random Phase jitter (12kHz - 20MHz)	ps, rms	1.43	1.43	1.43	1.44	1.43
Period jitter	ps, rms	1.81	1.60	1.60	1.57	1.52
Period jitter (10,000 cycles)	ps, pk-pk	13.6	12.1	12.0	11.7	11.7
Duty cycle	%	49.9	49.9	50.1	50.4	50.6
Rise time (20% - 80%)	ns	1.24	1.01	0.94	0.98	0.93
Fall time (80% - 20%)	ns	1.24	0.98	0.90	0.96	0.92
Amplitude	V	1.76	2.45	2.75	2.97	3.29
Current consumption (no load, output enabled)	mA	3.80	4.00	4.08	4.12	4.22
Current consumption (no load, output disabled)	mA	3.43	3.50	3.55	3.59	3.67



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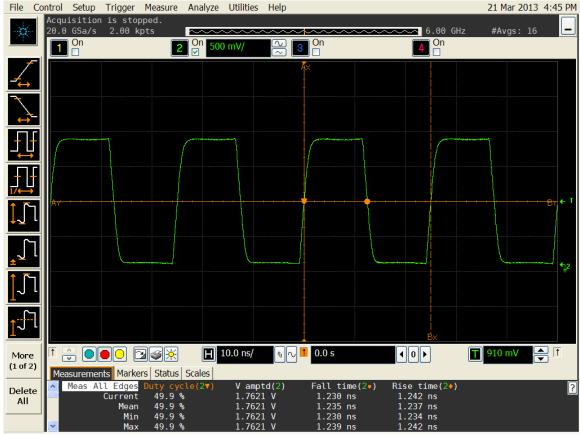


Figure 1. Duty cycle, Rise/Fall time and Amplitude 1.8V



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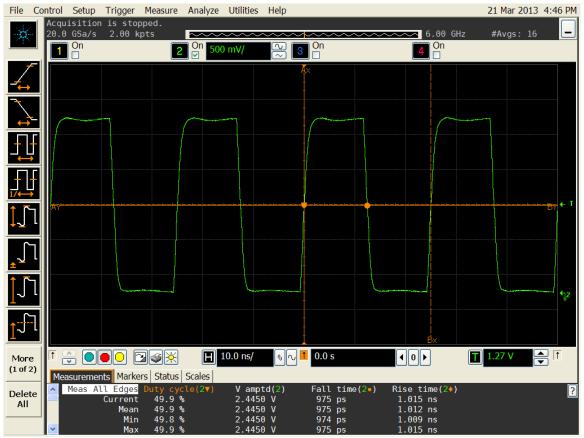


Figure 2. Duty cycle, Rise/Fall time and Amplitude 2.5V



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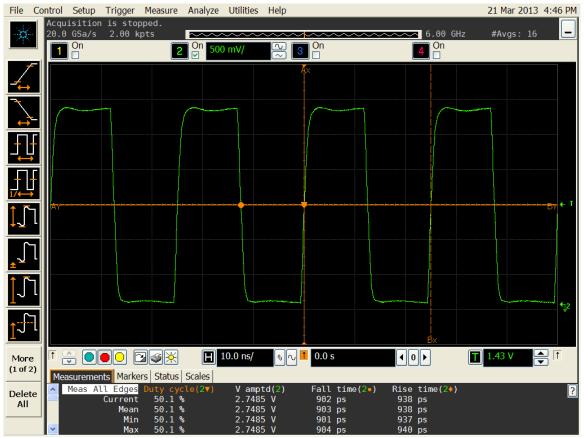


Figure 3. Duty cycle, Rise/Fall time and Amplitude 2.8V



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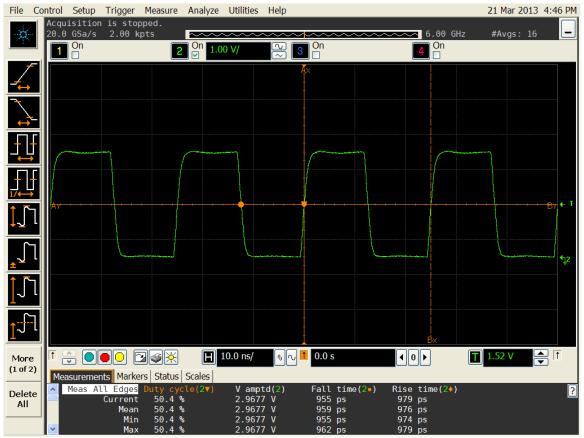


Figure 4. Duty cycle, Rise/Fall time and Amplitude 3.0V



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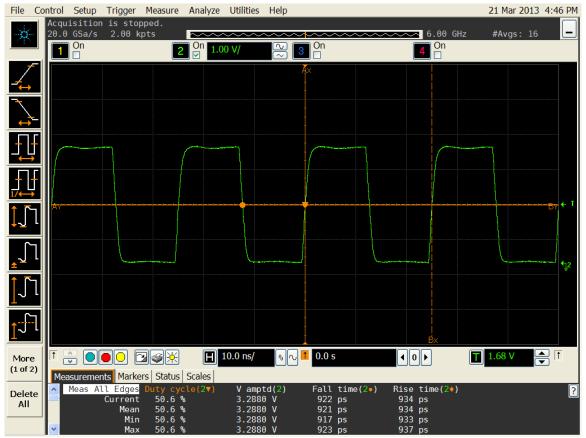


Figure 5. Duty cycle, Rise/Fall time and Amplitude 3.3V



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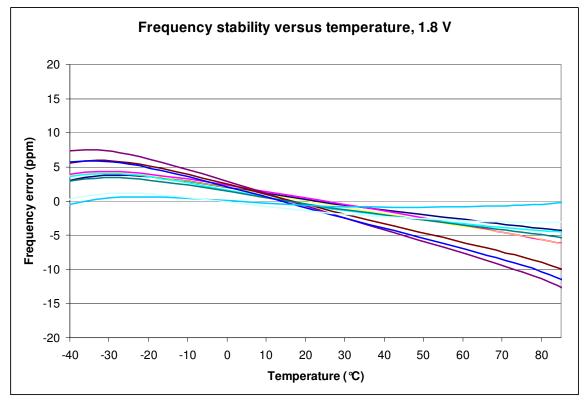


Figure 6. Frequency stability* versus temperature, 1.8 V

^{*}Please note that frequency stability in SiTime devices is not depended on output frequency.



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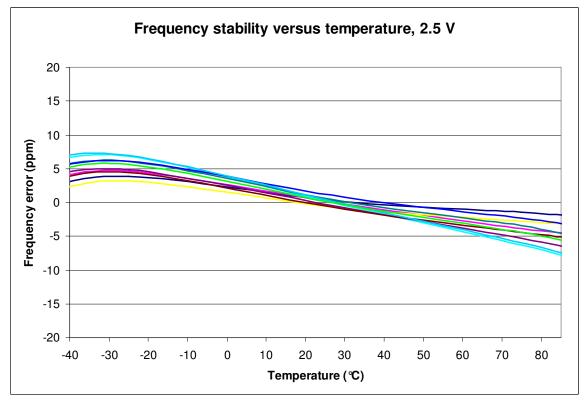


Figure 7. Frequency stability versus temperature, 2.5 V



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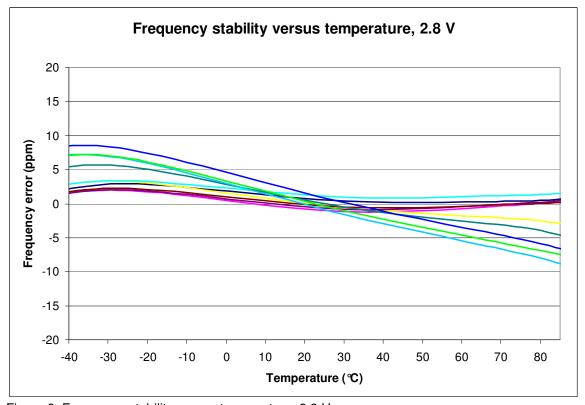


Figure 8. Frequency stability versus temperature, 2.8 V



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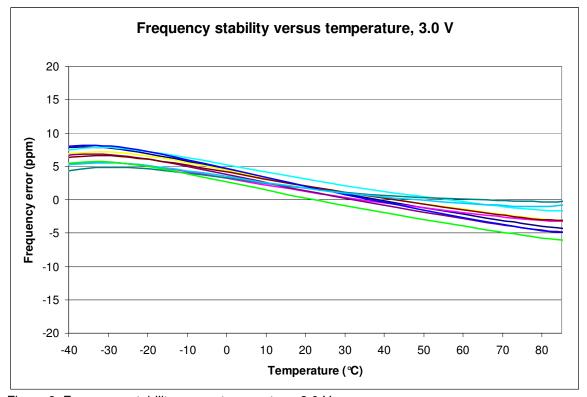


Figure 9. Frequency stability versus temperature, 3.0 V



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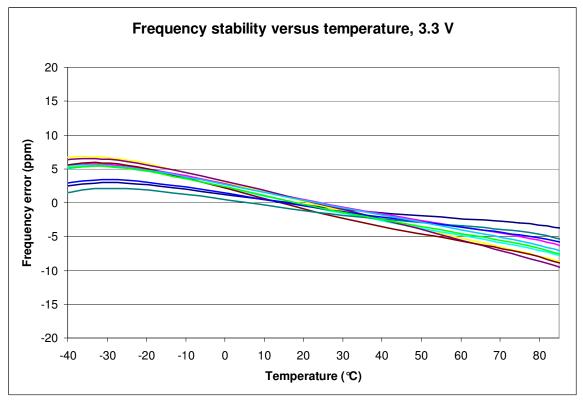


Figure 10. Frequency stability versus temperature, 3.3 V